



Dr. M.G.R. EDUCATIONAL AND RESEARCH INSTITUTE (Deemed to be University)

Maduravoyal, Chennai - 600 095. Tamilnadu, India.

(An ISO 9001-2015 Certified Institution)



MGR ASSOCIATION FOR GREAT INNOVATIVE CREATORS

Department of Electronics and Communication Engineering

Event Title	FDP on " Designing chips low power artificial intelligence		
Objective of Event	objective could be to facilitate knowledge sharing and collaboration among faculty members from different institutions or departments. The event might provide a platform for educators to exchange ideas, discuss best practices, and establish networks for future collaboration		
Chief Guest /Speaker Details	Prof.Kishore Burchandi, Professor, VNIT Nagpur Prof.Sreehari Rao Patri, Professor, VNIT Warangal Dr.Krishnakant Ayulur, Manager, NXP		
Date	22/04/2024 to 01/05/2024	Time	09.30 AM to 01.30 PM
Venue	Online	No. of Participants	55

REPORT

TITLE

Designing chips low power artificial intelligence

CONTENT

The Department of Electronics and Communication Engineering (ECE), in collaboration with the Electronics & ICT Academy at NIT Warangal, conducted a ten day virtual Faculty Development Program (FDP) on the theme of "Designing Chips with Low-Power Artificial Intelligence." This program coordinated by Dr. M. Swarna, ASP/ECE and supported by Ms. V. Menaka, Mr. K. Amuthantham, and Dr. G. Kavitha, all esteemed faculty members of the ECE Department. The inaugural session was on 22.04 2024. The event was graced by distinguished Persons, including Prof. Bidyadhar Subudhi, Director of NIT Warangal, who presided over the inaugural. We had 57 participants from various Educational Institutions like Easwari Engineering College, CMR Engineering College Hyderabad, Sri Manakula Vinayagar Engineering College and Paavai Engineering College.Two sessions were conducted per day to enrich the knowledge of the participants.

The following were the topics covered,

Day-1(22.4.2024)

In the First session Prof.Kishore Burchandi, VNIT Nagpur has illustrated the significance of the AI avenues for system designs. The second session speaker, Prof. Sreehari Rao Patri, NIT Warrangal demonstrated about the IC Design Facts- AI appeal. The Resource Speakers introduction was given by Dr.M.Swarna Asso.Prof/ECE.

Day 2 (23.4.2024)



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Dr.Krishnakant Ayulur, AMS demonstrated the advances in Energy efficient sensor interface circuits in the first session. Dr.M.A. Mushahhid, NXP semiconductor discussed about the Analog IC Design automation approaches using AI in the next session . The brief profile of the speakers were given by Dr. G. Kavitha, Asst.Prof/ECE

Day 3 (24.4.2024)

The first session addressed by Prof. K.Anitasheela about the Neural network pruning. In the next session, Dr. P.Muralidhar addressed the participants about the Hardware acceleration techniques The speakers introduction was given by Mrs. V. Menaka, Asst.Prof/ECE

Day 4 (25.4.2024)

In the First session Hanuman Reddy substantiated the need for Power management IC design – AI Augmentation. Dr P.Muralidhar has hands on training about Hardware acceleration Technique lab. The Resource Speakers introduction was given by Mr.K.Amuthanantham, Asst.Prof/ECE

Day 5 (26.4.2024)

Ms.A Radika, Group Director Cadence Design System USA elaborated on IC Design – AI approach to EDC in the first session. Krishnamahesh discussed about Low Power Memory Solutions in the next session The guest speaker introduction was given by Dr. M.Swarna, Asso.Prof/ECE

Day 6 (27.4.2024)

Mr. Krishna Mahesh karanam briefed the IC design ML approach in the first session. Dr M.Sathish /Dr Narendar had a discussion about Nanodevice Modelling in the next session.The Speakers introduction was given by Dr.G.Kavitha, AP/ECE

Day 7 (28.4.2024)

Dr Narendar & Dr M.Sathish illustrated with Nanodevice Modelling- Simulation in both sessions. The Speakers introduction was given by Dr.B.Swapna, AP/ECE

Day 8 (29.4.2024)

In the first session, Prof Faruq Hashmi about ML- IoT Application and in the second session, Dr Atul kumar Nishad discussed about the Energy Efficient nanoscale interconnect. The resource speaker introduction was given by Mr.K.Amuthanantham, Asst.Prof/ECE

Day 9 (30.4.2024)

Prof. Katkoori demonstrated AI driven Hardware Security in first session and Prof Hanuman Reddy elaborated AI in ASIC flow in the next session. The Guest speakers introduction was given by Mrs. V. Menaka, Asst.Prof/ECE

Day 10 (1.5.2024)

Prof R.Lavanya substantiated the various semiconductor materials and Prof. Sudeb das Gupta described about Energy Efficient Transistor in the following session. The resource speakers introduction was given by Dr.M.Swarna, Asso.Prof/ECE

The Valedictory was on 1.5.2024. Prof.Sreehari Rao Patri delivered the welcome address, followed by Dr.Swarna, introduced our valedictory guest Prof Dharmendra. The Rector, Dr.D. Viswanathan, and Prof.V.Cyril Raj, Additional Registrar, delivered the felicitation addresses. PProf.L.Ramesh Joint Registrar provided the valedictory address. Following that, participants provided feedback for the 10 days FDP . The event ended with a vote of thanks by Dr.U.Jayalatsumi. HOD/ECE. We extend our heartfelt gratitude to our Honorable President for their unwavering support and encouragement

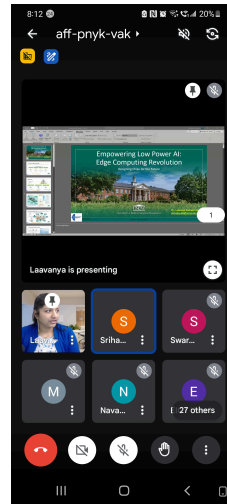


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throughout, and to all the executives for their relentless support and motivation in making this event a resounding success.



EVENT OUTCOME

Got knowledge about pedagogical innovation within the academic community. This could involve sessions on innovative teaching techniques, active learning strategies, or the integration of experiential learning methods into the curriculum. The goal would be to inspire faculty members to explore new approaches to teaching and learning that can enhance student engagement and outcomes.

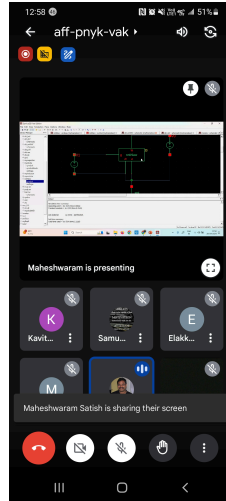
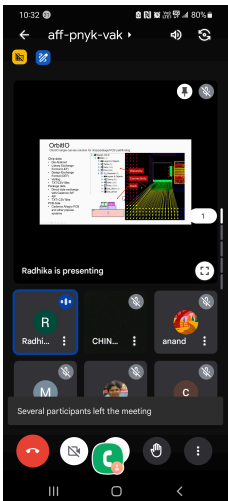
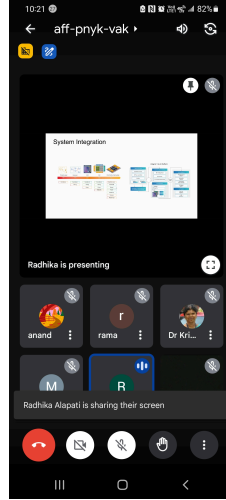
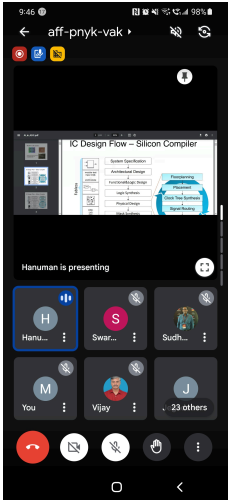
PHOTOS



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Dr. M. Swarna