



Dr. M.G.R.
EDUCATIONAL AND RESEARCH INSTITUTE
(Deemed to be University)
Maduravoyal, Chennai - 600 095. Tamilnadu, India.
(An ISO 9001-2015 Certified Institution)



MGR ASSOCIATION FOR GREAT INNOVATIVE CREATORS

Department of Electronics and Communication Engineering

Event Title	Workshop on "Analog and Digital System Design Using Cadence Tool"		
Objective of Event	The objective of the workshop on "Analog and Digital System Design Using Cadence Tool" is to provide participants with hands-on experience in designing, simulating, and verifying analog and digital circuits using Cadence software. It aims to enhance their		
Chief Guest /Speaker Details	Pulivarthi Rajendran, Application Engineer, Cadence		
Date	04/03/2025 to 05/03/2025	Time	10.00 AM to 04.00 PM
Venue	VLSI Lab	No. of Participants	50

REPORT

TITLE

Workshop on "Analog and Digital System Design Using Cadence Tool"

CONTENT

A workshop on "Analog and Digital System Design Using Cadence Tool" provides hands-on training in designing, simulating, and verifying integrated circuits using Cadence software. It covers both analog and digital design methodologies, helping participants understand schematic capture, circuit simulation, layout design, and verification processes. The workshop introduces Cadence Virtuoso for analog design, including SPICE simulations, layout creation, and DRC/LVS verification. For digital design, it focuses on RTL-to-GDSII flow, synthesis, timing analysis, and ASIC design using Cadence Encounter. Participants will gain practical exposure to industry-standard tools used in VLSI design, enhancing their understanding of electronic circuit behavior and fabrication constraints. The sessions aim to build expertise in designing optimized and manufacturable circuits while addressing real-world challenges in semiconductor design. This workshop is beneficial for students, researchers, and professionals.



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EVENT OUTCOME

The workshop enables participants to design, simulate, and verify analog and digital circuits using Cadence tools. They gain hands-on experience with Cadence Virtuoso for analog design and Cadence Encounter for digital synthesis. By the end, attendees will be proficient in SPICE simulations, layout design, DRC/LVS checks, and timing analysis, enhancing their VLSI and ASIC design skills, making them industry-ready for semiconductor and electronic system development.

PHOTOS



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Dr. V. Amutha